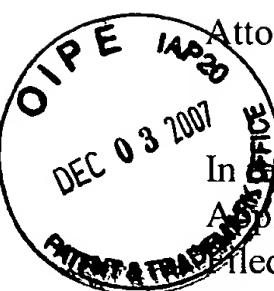


C O F C



Attorney Docket No. 5649-886

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re: Jung et al.
Application No.: 09/941,091
Filed: August 28, 2001

U.S. Patent No.: 7,246,250 B2
Issued: July 17, 2007

For: MEMORY DEVICE CONTROLS DELAY TIME OF DATA INPUT BUFFER IN
RESPONSE TO DELAY CONTROL INFORMATION BASED ON A POSITION OF A
MEMORY DEVICE RECEIVED FROM MEMORY CONTROLLER

November 30, 2007

Commissioner for Patents
Attn: Certificate of Correction Branch
P.O. Box 1450
Alexandria, VA 22313-1450

**REQUEST FOR ENTRY OF CORRECTIVE CERTIFICATE OF CORRECTION
UNDER 35 U.S.C §254 AND 37 C.F.R. §1.322**

Sir:

The Assignee of record for the above-referenced patent hereby requests, pursuant to 35 U.S.C §254 and 37 C.F.R. §1.322, that a Corrective Certificate of Correction be issued. This request is made in order to correct a mistake incurred through the fault of the U.S. Patent and Trademark Office.

*Column 12, Claim 20, Line 12: Please correct "controlling the delay" to read --
controlling a delay--*

No fee is believed due. However, the Commissioner is authorized to charge any deficiency or credit any overpayment to Deposit Account No. 50-0220.

The mistakes appearing in the patent are set forth with corrections on the Certificate of Correction enclosed herewith, and a return post card.

Respectfully submitted,

D. Scott Moore
Registration No. 42,011

Certificate

DEC 05 2007

of Correction

Myers Bigel Sibley & Sajovec, P.A.
P. O. Box 37428, Raleigh, NC 27627
Telephone: (919) 854-1400
Facsimile: (919) 854-1401
Customer No. 20792

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, Attn: Certificate of Correction Branch, P.O. Box 1450, Alexandria, VA 22313-1450, on November 30, 2007.

Jessica M. French

DEC 11 2007

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,246,250 B2
APPLICATION NO. : 09/941091
DATED : July 17, 2007
INVENTOR(S) : Jung et al.

Page 1 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In The Claims:

Column 9, Claim 3, Line 52: Please correct "memory device":
To read -- memory devices --

Column 10, Claim 8, Line 39: Please correct "according to the"
To read -- according to a --

Column 10, Claim 8, Line 43: Please correct "delay information"
To read -- control information --

Column 10, Claim 8, Line 44: Please correct "delay control"
To read --predetermined control --

Column 10, Claim 8, Line 46: Please correct "respond to the"
To read -- respond to an --

Column 11, Claim 12, Line 15: Please correct "receiving delay control"
To read -- receiving the control --

Column 11, Claim 12, Line 17: Please correct "received delay control"
To read -- received control --

Column 12, Claim 20, Line 12: Please correct "controlling the delay"
To read -- controlling delay -- .. controlling a delay --

Column 12, Claim 24, Line 51: Please correct "that is based on position"
To read -- that is based on a position --

Column 14, Claim 34, Line 37: Please correct "operating an integrated circuit memory"
To read -- operating a memory --

DEC 11 2007

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 7,246,250 B2
APPLICATION NO. : 09/941091
DATED : July 17, 2007
INVENTOR(S) : Jung et al.

Page 2 of 2

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Column 14, Claim 34, Line 41: Please correct "information from that is"
To read -- information that is --

This certificate supersedes Certificate of Correction issued October 16, 2007.

Signed and Sealed this

Thirteenth Day of November, 2007



JON W. DUDAS
Director of the United States Patent and Trademark Office

DEC 11